

Curriculum Vitae

Prof. Herman E. Maes



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Herman E. Maes was born in Leuven, Belgium on August 15, 1947.

He received the M.Sc. degree in electrical engineering in 1971 and the Ph.D. degree in 1974 (under the supervision of Professor Roger van Overstraeten), both from the Katholieke Universiteit Leuven (KULeuven) in Belgium. From 1971 until 1974, he was a Research Assistant (Fellow of the National Fund of Scientific Research of Belgium, NFWO) in the laboratory for Physics and Electronics of the University of Leuven.

In 1974, he was granted a CRB fellowship by the Belgian American Educational Foundation and spent 14 months at the Electrical Engineering Research Laboratory of the University of Illinois, Urbana, as a Research Associate to Professor C.T. Sah.

From 1975 until 1985, he was with the ESAT laboratory at the University of Leuven as a Senior Research associate of the Belgian National Fund for Scientific Research and a lecturer at the University.

Since 1985, he has been a part-time Professor at the Faculty of Engineering of the KULeuven, teaching courses on Semiconductor Physics, on Materials Characterization techniques and on Nanotechnology.

In 1985, he joined the newly established R&D Laboratory of the Interuniversity Micro-Electronics Center (imec) in Leuven, Belgium, as Head of Analysis and Reliability. In 1990 he became an Associate Vice-President in IMEC, in 1998 Vice-President, heading the Division on Silicon Technology and Device Integration and in 2002 Senior Vice-President and Director of the INVOMECE Division (ASIC and Design Services, Industrialization and Training in Micro-electronics) at imec.

In 2012 he became Professor Emeritus at the KULeuven. He retired from imec on 1 January 2013 and acts as a self-employed consultant.

Herman Maes has been elected a Fellow of the IEEE (Institute of Electrical and Electronics Engineers) in 1997 *for contributions in the field of non-volatile silicon memory devices and for contributions to MOS Reliability Physics*.

He has authored or co-authored more than 450 international technical papers (h-index of 37) including 3 books, 10 book chapters and contributed to over 500 conference papers including more than 50 invited papers. Twenty (20) of these papers received a Best Paper award from a major conference. H. Maes holds 18 patents. He has guided 40 students to the Ph.D. degree. His research interests covered **non-volatile memory devices, silicon integration, nanoelectronics, physics of semiconductor devices, reliability issues and physics of integrated circuits**.