

Short Bio of Roberto Nonis

Roberto Nonis was born in Udine, Italy, 1977. He received the Laurea degree in management engineering from University of Udine in 2002, working on phase noise modeling in PLL-based frequency synthesizers. He received the Ph.D. degree from the same University in 2007, working on modeling and design of low power integrated circuits for frequency synthesis. He spent one year as Post-Doc at Agere Systems (now LSI), Allentown, Pennsylvania (USA) working on the design of high-speed, low power dynamic comparators for ADCs. He is currently working at Infineon Technologies AG, Villach, Austria, as technical project leader focusing on analog and digital frequency synthesizers and high-speed serial interfaces for automotive applications.